

FIGURE 1

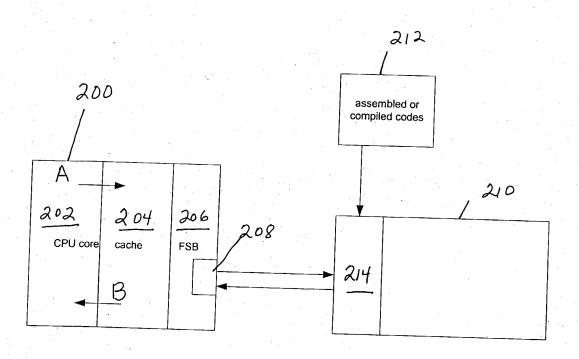


FIGURE 2

300

load a local memory resource for a processor core of NUMA design with a functional test program, the processor being able to access non-local memory resources through a link control mechanism

302

provide an external path for the test signals generated by the processor core during execution of the functional test program

FIGURE 3

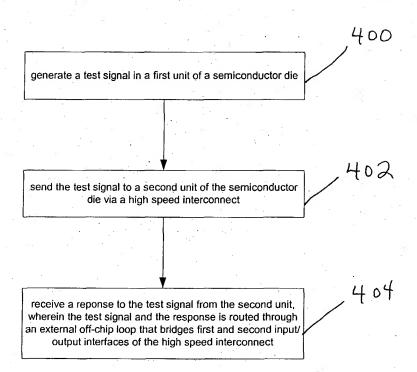


FIGURE 4

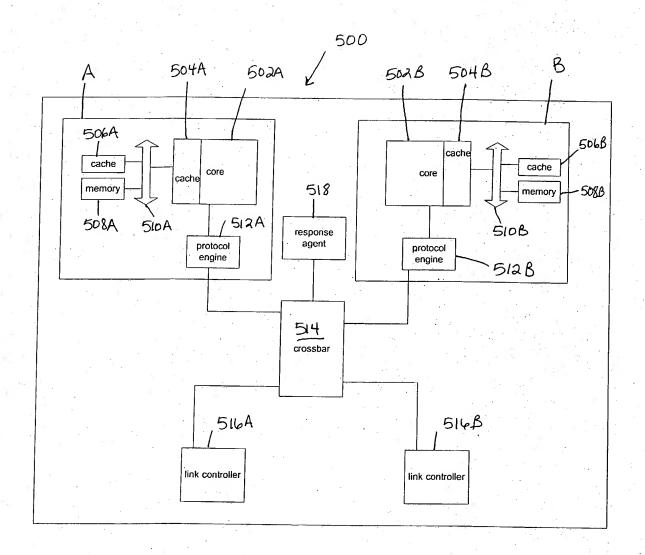


FIGURE 5

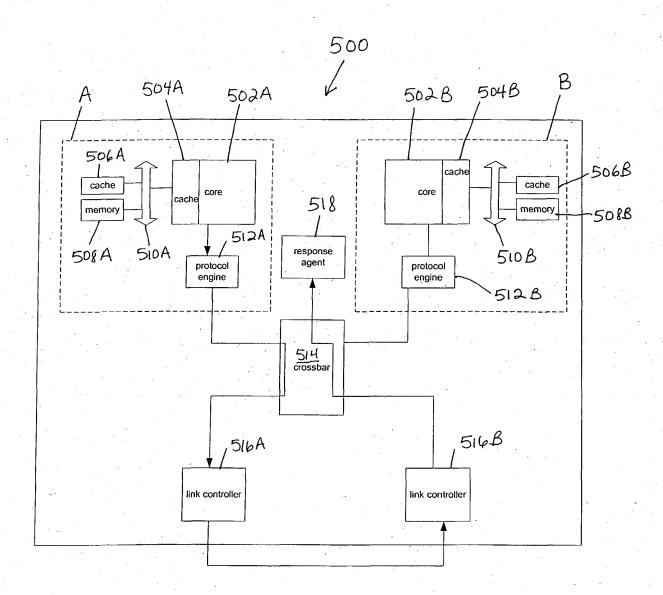


FIGURE 6

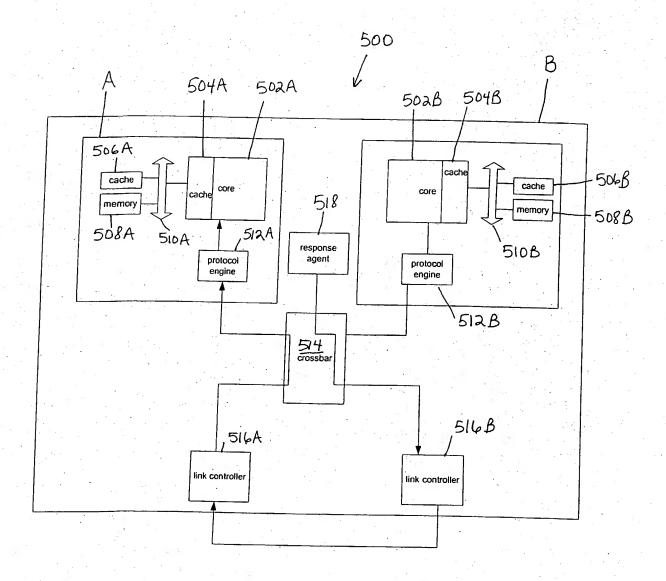


FIGURE 7